

A resizing circuit resizes an output image from an image pickup element to be suitable for an LCD (liquid crystal display) panel, and inputs the resized image data to a P/S (parallel-to-serial) converter through a memory interface, an FIFO (first-in, first-out) memory, a dot sequential conversion circuit and a sync signal addition circuit. The P/S converter converts eight-bit parallel data into one to four serial data, and transfers the converted data to an LCD control circuit. The LCD control circuit parallelizes the input serial data, converts it into an analog image signal, and transfers the analog image signal to the LCD panel.

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